<u>Imperial Valley College</u>
COURSE SYLLABUS FOR DIGITAL LOGIC CIRCUITS (ELTR 240)
3.0 Credit Units. CRN: Spring 2019
Ricardo Jiménez, Instructor.

Course Description:
A continuation of ELTR220. The advanced study of applied digital electronic systems such as those found in computing, audiovisual, and other mechatronics equipment.

Lecture & Laboratory Course Goals And Objectives:
Upon successful completion of this course, the student will be able to:
1. Understand the theory of decimal, binary, and hexadecimal codes, which is
the essential factor in making digital circuits operate properly.
2. Analyze and reduce digital circuits using Boolean Algebra and Karnaugh Maps.
3. Explain the operation of combinatorial circuits in different configurations.
4. Apply K Maps in digital electronic circuits using logic gates.
5. Compare and contrast the newer digital logic families
6. Compare PLDs systems based on GAL architectures
7. Apply microcontrollers for control applications
8. Write Code for PIC microcontrollers using PBP3 or C language.
9. Analyze new devices and systems proposed by authors in journals and trade
magazines and appraise the value of these advances for redesign of systems.

Class Hours:
Fridays 8:05 A.M.—11:50 A.M., Room 3110.

Detail Course Schedule by weeks:
1. Number systems and Codes
2. Boolean Algebra and Reduction Techniques
3. Digital Logic Families
4. Decoders, Encoders, and Comparators
5. Multiplexers and Demultiplexers
6. Flip-Flops for Sequential and Counter Circuits
7. Mid Term Exam and Project
8. Microcontrollers and Assembler Language
9. Architecture of PIC and Arduino microcontrollers
10. Instruction set for PIC microcontrollers
11. Design of algorithms for digital applications
12. Design of methods for advanced applications
13. Methods for time-period measurements
14. Introduction to PLCs and Ladder Logic
15. Multisim software for digital applications
16. Final Examination and Project

Discussion Of Assignments And Instructional Methods:
Discussion of assignments and instructional methods will be a combination of all methods of instruction, which can be classified as telling, lecturing, or discussing; showing or demonstrating.

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Statement Of Grading Procedures:
1. Homework, Assignments: 10%
2. Lab. Experiments and Reports: 50%
3. Mid-Term Exam: 20%
4. Final Exam: 20%

Attendance Policy:
A student may be excluded (drop) from further attendance in a class during the semester when absences after the close of registration have exceeded the number of class hours, which the class meets per regular semester week (5 hours 30 minutes = two classes).
A student who is tardy two times may be considered as having been absent one class.
More than Two absences (5 hours 30 minutes) after the close of registration: Drop

Textbooks :
The PIC Microcontroller Engineer's Notebook, Vol II, 1st Edition, © 2018 by Ricardo Jimenez. ISBN: 978- 1-7325906-0-1. *[available at amazon.com]*
Digital Electronics Principles and Applications, Third Edition. Tokheim. Mc Graw Hill. ISBN: 0-07-830981-6.

Required Materials:
Scientific Calculator or similar Cell phone App.
All other materials with the exception of textbooks and calculator will be supplied.
Accommodations For Disabilities:
Any student with a documented disability who may need educational accommodations should notify the instructor or the Disabled Student Programs and Services (DSP & S) office as soon as possible.
Policy On Plagiarism And Cheating:
If cheating or plagiarism is discovered, a student may be dropped from the course with a grade of "F".
All photos from the board will be uploaded to a fb group to keep a record of the lectures and Lab experiments. I will be audio recording randomly this class for quality and archiving purposes.